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[ARM Architecture Introduction: Cortex M0, Cortex M1, Cortex M3 \u0026 Cortex M4](#)~~How to Choose your ARM Cortex M Processor~~
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Getting started with Arm Cortex-M software development and Arm Development Studio **ARM University Program DSP Workshop at Embedded World 2015 - part 1** ~~Arm Cortex M0 Workshop~~

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~~Cortex-M0—Arm Developer~~

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- ARM Debug Interface v5, Architecture Specification (ARM IHI 0031) Note A Cortex-M0 implementation can include a Debug Access Port (DAP). This DAP is defined in v5.1 of the ARM Debug interface specification, or in the errata document to Issue A of the ARM Debug Interface v5 Architecture Specification.
- Application Binary Interface for the ARM Architecture (The Base Standard) (IHI0036 ...

~~Cortex-M0 Technical Reference Manual~~

Designed for smart and connected embedded applications, especially where size matters, the Cortex-M0 is the smallest Arm processor available, making it ideal for use in simple, cost-sensitive

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devices. Cortex-M0 is available to access for a low upfront cost with Arm DesignStart.

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The Cortex-M0+ processor is an entry-level 32-bit ARM Cortex

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processor designed for a broad range of embedded applications. It offers significant benefits to developers, including:

- A simple architecture that is easy to learn and program.
- Ultra-low power, energy-efficient operation.
- Excellent code density.

~~Cortex-M0+ Devices—ARM architecture~~

The ARM Cortex-M is a group of 32-bit RISC ARM processor cores licensed by Arm Holdings. These cores are optimized for low-cost and energy-efficient microcontrollers, which have been embedded in tens of billions of consumer devices. The cores consist of the Cortex-M0, Cortex-M0+, Cortex-M1, Cortex-M3, Cortex-M4, Cortex-M7, Cortex-M23, Cortex-M33, Cortex-M35P, Cortex-M55.

~~ARM Cortex-M—Wikipedia~~

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